

2015

- Kavya T S, Sunil Jacob, "Energy efficient TRDMA over multipath channels", Communication on applied Electronics, pp 6-11, Aug 2015
- Dinta Denny , Sunil Jacob, V Satheesh Prabhu ,Jerry Kuriakose," Performance Analysis of Hierarchical QAM in an OFDM Transmission System" ,International Journal Of Advanced Information Science and Technology, September 2015
- Neha Ibrahim, Vinoj P.G, Sunil Jacob " A Novel Methodology for Detection and Correction of Errors in Memory using Parity Matrix Code (PMC)",IJARCSSE, Volume 5, Issue 8, pp 350-360, August 2015
- Aparna Babu, Parvathi R, Sunil Jacob" Effective noise acoustic echo reduction system based on adaptive algorithm for speech signals", International Journal of Applied Engineering Research, pp 37110-37116, 2015
- Divya K, Vinoj P G, Sunil Jacob, "Testing DRAM and correcting errors by using adaptive technique", International Journal of Engineering Research and Applications, ISSN:2248-9622, pp 90-95, Nov 2015
- Harigovind, Vinoj P G, "A novel architecture for high quality random number generation based on enhanced WELL Method," International journal of Science and research, ISSN:2319-7064, pp 1064-1068, Oct 2015.
- Reenu Tomy, Vinoj P G, "DPA Resistant AES Using a true random based LFSR technique", International Journal of Science and research, ISSN:2319-7064, pp 1865-1868, Nov 2015
- Jerrin Paul M, Hazel Elsa John, "Low power self-timed TCAM based on overlapped search mechanism with IP filter implementation", International Journal of Science and research, ISSN:2319-7064, pp 1332-1335, Sep 2015
- Suvarna S Menon, Anandhi V, "Energy Efficient Routing Based on Sleep Schedule for Wireless Sensor Network", International Journal Of Emerging Technology and Advanced Engineering, ISSN 2250-2459, pp 78-86, Oct 2015
- Rini Francis, Dr. Sunil Jacob, Retty George, "Low Power LFSR with LACG and Double Edge triggered Flip flop and comparison of different clock gating techniques," International Journal Of Emerging Technology and Advanced Engineering, ISSN 2250-2459, pp 347-349, Oct 2015.
- Anjali Ramachandrn, Praveena S Kammath, Sunil Jacob, "Integration of CG and PG: A Novel Technique using DET-Flip Flops", International Journal Of Emerging Technology and Advanced Engineering, ISSN 2250-2459, Oct 2015, pp 159-164.

- Jibi K Kurian, Praveena S Kammath, "Improved Power reduction and Aging Mitigation using gate replacement and voltage scaling techniques", International journal of Science and research, ISSN:2319-7064, pp 1118-1124, Oct 2015.
- Cini George, Vrinda V Gopal T, " Multiple Error Recovery in FIR filter using Reduced-Precision Redundancy(RPR)", International journal of Science and research, ISSN:2319-7064, pp 92-96, Nov 2015.
- Lisha Shoby, Deepa B, Sunil Jacob, "Low power critical path redesigned ECRL Pipeline implementation", International Journal of Advances in Engineering & Technology, Vol 8, Issue 5, Oct 2015, pp 7828-835.
- Elizabeth Joseph Mattam, Deepa Balakrishnan, "Area Optimized Double Precision IEEE Floating Point Adder", International journal of Science and research, ISSN:2319-7064, Sep 2015, pp 344-347
- Neethu Anna Sabu, Sreeja K A, "A low power highly applicable approach for caches based on STT-RAM technology", International journal of Science and research, ISSN:2319-7064, Sep 2015, pp 1325-1327
- Krishnapriya Vinod M, Anandhi V. , "Optimal Design and Implementation of Reversible Random Access Memory", International Journal of Science and Research (ISSR), Volume 4 Issue 10, October 2015
- Suvarna S Menon, Anandhi V, "Energy Efficient Routing Based on Sleep Schedule for Wireless Sensor Networks", International Journal of Emerging Technology & Advanced Engineering, Volume 5, Issue 10, October 2015

2014

- Amrutha A, Sunil Jacob, Vrinda V Gopal, " Implementation of Orthogonal Frequency Division Multiplexing Transceiver on FPGA", International Journal of VLSI and Embedded System, pp 1341-1345, Nov 2014
- Diljith Murali, Sunil Jacob, K Padmakumar, " Floating Point Addition Using Low Power CSA", International Journal of VLSI and Embedded System, pp 1377-1383, Nov 2014
- Akhil A, Sunil Jacob. " Design Of 9 Bit SAR ADC Using High Speed and High Resolution Open Loop CMOS Comparator in 180nm Technology with R-2R DAC Topology", International Journal of VLSI and Embedded System, pp 1391-1396, May 2015

- VishnuPrasad S, Sunil Jacob,Vivek Anand,“ Mil-Std 1553 Remote Terminal Protocol Processor Implementation Using FPGA “, International Journal of VLSI and Embedded System,pp 1408-1412,May2014
- Deepak Joseph Babu, Sunil Jacob,“ Design of A Low Voltage Low Power Double Tail Comparator In 180nm CMOS Technology”, American journal of Engineering Research, pp 15-19,2014
- Sandhya Venugopal, Sunil Jacob, Praveena Kammath,“ Design Of Up/Down Counter Based on Dual Mode Logic and Low Power Hybrid Dual Mode Dynamic Flip Flop, American Journal of Engineering Research (AJER)”, pp 1-6, September 2014.